



PATENT APPLICATION

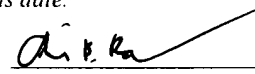
IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Munir H. Nayfeh
Serial No.: 09/496,506
Conf. No.: 6344
Filed: February 2, 2000
For: SILICON NANOPARTICLE FIELD
EFFECT TRANSISTOR AND
TRANSISTOR MEMORY DEVICE
Art Unit: 2811
Examiner: Sara R. Crane

I hereby certify that this paper is being deposited with the United States Postal Service as FIRST-CLASS mail in an envelope addressed to: Mail Stop AMENDMENT, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this date.

May 16, 2005

Date
F-CLASS.WCM
Appr. February 20, 1998


Registration No. 43,874
Attorney for Applicant

RESPONSE TO NOTICE OF INCOMPLETE REPLY

Mail Stop AMENDMENT
Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450


Dear Sir:

In response to the Notice of Incomplete Reply mailed May 5, 2005, Applicant submits herewith a copy of reference Brus, L., "Luminescence of Silicon Nanocrystals and Porous Silicon", Jpn. J. Appl. Phys., Vol. 34 (1995) Suppl. 34-1, pp. 5-7. This is the reference stated to be missing from the Supplemental Information Disclosure Statement filed April 8, 2005.

Applicant respectfully submits that the submission of this reference is fully responsive to the Notice of Incomplete Reply, and thus requests full consideration of the Response filed December 29, 2004 and the Supplemental Information Disclosure Statement filed April 8, 2005. If any outstanding issues exist, the Examiner is requested to contact Applicant's attorney at the number listed below.

Respectfully submitted,

GREER, BURNS & CRAIN, LTD.

By 
Arik B. Ranson
Registration No. 43,874

Customer No. 24978

May 16, 2005

300 South Wacker Drive
Suite 2500
Chicago, Illinois 60606
Telephone: (312) 360-0080
Facsimile: (312) 360-9315

P:\DOCS\1201\63407\941833.DOC